

M-Modules for Analog Process I/O

	I/O Lines	Current/Voltage	Resolution	Acquisition/ Conversion Time	Miscellaneous	Front Connector	Isolation	Consumption typ.	Software
M62N	16 outputs	Current or voltage	15/16 bits	10.5 μ s max. @ -40..+85°C		25-pin D-Sub	Yes	476 mA .. 661 mA depending on current/voltage	Windows, Linux, QNX, VxWorks, RTX, OS-9
M37N	4 outputs	Current or voltage	16 bits	10.5 μ s max. @ -40..+85°C	Simultaneous update, external trigger	25-pin D-Sub	Yes	270 mA	Windows, Linux, QNX, VxWorks, RTX, OS-9
M36N	8/16 inputs	Current or voltage	16 bits	8.5 μ s max. @ -40..+85°C	I/O customizable in FPGA (also Nios)	25-pin D-Sub	Yes	500 mA	Windows, Linux, QNX, VxWorks, RTX, OS-9
M35N	8/16 inputs	Current or voltage	14 bits	< 8 μ s	Improved version of M34/M35	25-pin D-Sub	Yes	190 mA (no DC), 670 mA (with DC)	Windows, Linux, QNX, VxWorks, RTX, OS-9
M33	8 outputs	Current or voltage	12 bits	10 μ s	Simultaneous update	25-pin D-Sub	Yes	140 mA (no DC), 950 mA (with DC)	Windows, Linux, QNX, VxWorks, RTX, OS-9
M199	FPGA/Nios-based main M-Module with 32 MB SDRAM and 8 MB Flash for integration of user-defined I/O on a USM Universal Submodule plugged onto the main module					1x 50-pin SCSI		Depending on USM	Depending on IP core functions