XMC, PMC and PC-MIP® Modules

Embedded Solutions

	Function	Intelligence	I/O Lines	Memory	Miscellaneous	Front Connector	Isolation	Consumption typ.	Software
XMC									
P699	Main module for integration of user-defined I/O	FPGA/Nios	Depending on IP core and USM configuration	32MB SDRAM 4MB Flash	Based on USM Universal Submodule concept	1x 50-pin SCSI	Depending on USM	Depending on FPGA and USM configuration	Depending on IP core functions
P602	Quad Redundant Gigabit Ethernet		4 10/100/1000Base-T channels		2 XMC connectors with 1 x4 PCIe link on each connector	4x RJ45	Yes	+3.3V: 100mA +5V: 1.4A +12V: 600mA	Windows, Linux
P601	Quad Redundant Gigabit Ethernet		4 10/100/1000Base-T channels		1 XMC connector with 2 x4 PCIe links	4x RJ45	Yes	+3.3V: 100mA +5V: 1.4A +12V: 600mA	Windows, Linux
PMC									
P599	Main module for integration of user-defined I/O	FPGA/Nios	Depending on IP core and USM configuration	32MB SDRAM 2MB Flash	Based on USM Universal Submodule concept	1x 50-pin SCSI	Depending on USM	+3.3V: 82mA +5V: 109mA	Depending on IP core functions
P598	Conduction-cooled main module for integration of user-defined I/O	FPGA/Nios	Depending on IP core and USM configuration	32MB SDRAM 2MB Flash	Based on USM Universal Submodule concept		Depending on USM	+3.3V: 76mA +5V: 118mA	Depending on IP core functions
P518	Frame Buffer Interface	FPGA	VGA or DVI (front), LVDS (rear)	16MB SGRAM		1x DVI	No	+3.3V: 300mA +5V: 10mA	Linux, Windows 2000/XP
P517	Graphics Accelerator	SM731	DVI-I, S-Video I/O, Composite Video O (front) 2x LVDS (rear)	16MB SGRAM		1x DVI, 1x 9-pin D-Sub	No	+3.3V: 1.4A +5V: 900mA	Windows 2000/XP, OS-9, XiBase9
P512	Reflective Memory		1 LVDS channel (TX/RX)	32MB SDRAM	Usable in fully connected mesh	1x 50-pin SCSI	No	+3.3V: 143mA +5V: 109mA	Windows, Linux, VxWorks, QNX
P511	Dual Fast Ethernet	FPGA (Nios optional)	2 10/100Base-T channels	32MB SDRAM 2MB Flash	Based on USM Universal Submodules	2x RJ45 cable to 50-pin SCSI	1500 VAC	+3.3V: 139mA +5V: 40mA	Windows, Linux
P507	Quad RS422/485	FPGA (Nios optional)	4 channels full and half duplex	32MB SDRAM 2MB Flash	Based on USM Universal Submodules	1x 50-pin SCSI	500 VAC	+3.3V: 9mA +5V: 120mA	Windows, Linux
P506	Quad CAN bus	FPGA (Nios optional)	4 independent channels CAN 2.0A/B	32MB SDRAM 2MB Flash	Based on USM Universal Submodules	1x 50-pin SCSI	1500 VAC	+3.3V: 15mA +5V: 240mA	Windows, Linux

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XMC, PMC and PC-MIP® Modules

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PC-MIP									
P16	Dual 100Base-T Ethernet	i8259	2 full-duplex channels			2x 8-pin RJ45	Yes	+3.3V: 300mA	Linux, QNX, VxWorks, OS-9
P14	IEEE 1394 OHCI FireWire Controller	TI TSB43AA22 OHCI PHY/Link Layer Controller	1 channel		400 Mbits/s	6-pin JFW	No	+5V: 100mA +3.3V: 300mA +12V: 100mA	Windows NT/2000/XP
P13	48-bit TTL I/O Interface		48 (6 groups input or output)		Fast I/O with line termination	36-pin half-pitch D-Sub	No	+5V: 1.1A +3.3V: 68mA	Windows, Linux, VxWorks, RTX, OS-9, QNX
P11	Quad RS422/485 UART	16C950	4 asynchronous full-duplex channels			26-pin half-pitch D-Sub	No	+5V: 130mA +3.3V: 40mA	Windows, Linux, VxWorks
P10	Quad RS232 UART	16C950	4 asynchronous full-duplex channels		TTL level at rear alternatively	26-pin half-pitch D-Sub	No	+5V: 130mA +3.3V: 40mA	Windows, Linux, VxWorks, OS-9
P5	Intelligent CAN Interface	i82527 MC68331	1 channel full and extended CAN		CANopen master/slave software on board (Vector)	9-pin D-Sub	Yes	+5V: 470mA max. (2 channels)	Windows, Linux, VxWorks, RTX, OS-9, QNX